

# ECE 272: Term Schedule for Lab Assignments

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## 1 Schedule

Week	Section Title	Documents Due
1	Section 1 - Logic with 7400 Series Chips	Nothing
2	Section 2 - Basic Combinational Logic with the Digital Logic Board	Section 1 Study Questions
3	Section 3 - Combinational Logic Design: Custom Remote Control	Section 2 Study Questions, Section 3 Pre-lab
4	Section 4 - Combinational Logic Design: 7 Segment Display	Section 3 Study Questions, Section 4 Pre-lab
5		Nothing
6	Section 5 - Verilog: Modular Combinational Logic Using HDL	Section 4 Study Questions, Section 5 Pre-lab
7	Section 6 - Finite State Machine Using HDL	Section 5 Study Questions, Section 6 Pre-lab
8		Nothing
9	Section 7 OR 8 - Final Design Project	Section 6 Study Questions, Section 7 Pre-lab
10		Section 7 or 8 Study Questions

## 2 Grading

1. *Pre-lab* — Each Pre-lab will be worth 10 points, regardless of how many weeks are allocated to that section. Pre-labs do not need to be typed, but they must be turned in during the beginning of lab. No late work is accepted.
2. *Lab Credit* — Each lab is worth  $\frac{10\text{points}}{\text{week}}$ . i.e. (Section 1 is worth 10 points but section 4 is worth 20 points). Credit is assessed for lab credit during the week when work begins on the new section. There is partial credit for this section, but no late work will be accepted.
3. *Study Questions* — Each set of study questions will be worth 10 points, regardless of how many weeks are allocated to that section. Study questions are to be typed, printed, and turned in during the beginning of lab. No late work is accepted.
4. *Challenges* — Challenges are extra credit projects at the end of each section, worth a maximum of 5 points for each section. Challenges are given partial credit based off of how much effort is put into verifying a robust solution for the project. For example, a solution with no part numbers, resistor tolerances, or simulation results might be correct, but only a  $\frac{2}{5}$  score would be given. Challenges must be turned in with the study questions.