Lab 4 will use the T Flip-Flop (TFF) to make a counter. When put in series, each TFFs cuts the clock speed in half.

1. Given CLK oscillates at 10MHz, what is the frequency of CLK 2 and CLK 4?
2. Write the 3-bit value stored in the CLKs for each timestep, t1 -> t8.
3. If another T flip-flop was added in series, what would its output frequency be?