Hillsboro RFIC Design Engineering Intern

Job Summary:

- Design and development of RF switches and/or LNA for cellular applications using SOI and CMOS
- Job responsibilities include working with and supporting Design Engineers to help simulate integrated circuit and laminate designs
- Document simulation infrastructure process used to simulate performance of RFIC designs
- Generate and simulate EM models in HFSS
- Use of Test benches including Sig Gen, Spec An, Power Meter, VNA and probe-station to test and evaluate designs.

Job Qualifications:

- Course work (including lab) in RF circuit design a must, good knowledge of S-parameters, transmission lines and Smith chart. Other desired course work includes Analog circuit design, Integrated Circuit design, Electromagnetics, and Solid-state Devices.
- Good communication skills both verbal and written - good presentation skills, ability to clearly communicate technical and/or logistic issues/solutions
- Experience with design tools such as ADS, Cadence, HFSS/Sonnet/IE3D (EM tool)
- Experience with RF measurement equipment, such as spectrum analyzers and network analyzers, is highly beneficial.
- Pursuing MSEE, PhD degree or equivalent in a related engineering discipline